

Remarks

Amendment of Fig.4:

5 As described in the paragraph [0020] "When the channel 58 is formed, the storage unit 46 will be electrically connected to the control unit 48.", it is clear from Fig.3 that one end of the channel 58 is connected to the electrode 64, and another end of the channel 58 is floating. Concerning an equivalent circuit of the storage unit 46, one end of the
10 storage unit 46 should be floating instead of being connected to the ground. No new matter is introduced. In light of this amendment, the subject matter that the applicant regards as his invention is more clearly and precisely described. Allowance of the amended Fig.4 is hereby requested.

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Amendment of Fig.5:

As mentioned above, one end of the storage unit 46 should be floating. It is well-known that the storage unit 46 has a dielectric oxide layer
20 54. That is, the storage unit 46 is capable of functioning as a capacitor. As described in the paragraph [0021] "When the control gate 50 receives the voltage V_p with a value that is large enough to form the channel 58 in the substrate 44, the storage unit 46 will be electrically connected to the control unit 48 due to the formation of the channel 58. Meanwhile,
25 the equivalent parasitic capacitance increases because the number of the connected elements increases, which means the memory cell capacitance C_{cell} will have a larger value. While when the storage unit 46 is not electrically connected to the control unit 48, the memory cell capacitance C_{cell} will have a relatively smaller value.", it is obvious
30 that the control gate 50 of the storage unit 46 corresponds to the voltage V_p , and one end of the memory cell capacitance C_{cell} should correspond to the voltage V_p instead of the ground. No new matter is introduced.

In light of this amendment, the subject matter that the applicant regards as his invention is more clearly and precisely described. Allowance of the amended Fig.5 is hereby requested.

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Respectfully Submitted,



Date: 11/19/2003

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